



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Keyur B. Desai

§ Atty. Dkt. No.: 5681-81400

Serial No.: 10/702,150

§ § Examiner: Unknown

Filing Date: November 4, 2003

§ § Art Unit: 2857

For: Using and Generating Finite State
Machines to Monitor System Status

§ § I hereby certify that this correspondence is being deposited with the
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13
B. Noel Kivlin
Registered Representative

**TRANSMITTAL OF POWER OF ATTORNEY AND
NOTICE OF CHANGE OF ADDRESS**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please find attached a Power of Attorney with regard to the above-identified patent application. Applicant respectfully requests the Commissioner to change the correspondence address for the above identified patent application. The old correspondence address was:

Konrad Raynes Victor & Mann, LLP
315 South Beverly Drive
Suite 210
Beverly Hills, CA 90212

The new correspondence address is:

B. Noël Kivlin
Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.
P.O. Box 398
Austin, Texas 78767-0398
(512) 853-8840

If there are any questions regarding this matter, please contact me at the telephone number provided below.

Respectfully submitted,



B. Noël Kivlin
Registration No. 33,929

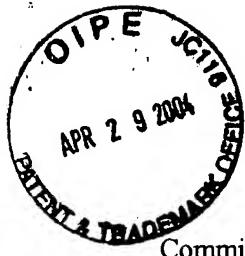
Meyertons, Hood, Kivlin, Kowert & Goetzl, P.C.

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Date: 4/20/04



POWER OF ATTORNEY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUN MICROSYSTEMS, INC. hereby revokes any previous Powers of Attorney and appoints the following attorneys and/or agents in connection with the patent applications and patents identified in Appendix A, owned by SUN MICROSYSTEMS, INC., that are filed with the United States Patent and Trademark Office:

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Anirma R. Gupta	Reg. No. 38,275	Joseph A. Twarowski	Reg. No. 42,191
Noreen A. Krall	Reg. No. 39,734		

each of said attorneys being employed by Sun Microsystems; and

Mark K. Brightwell	Reg. No. 47,446	Lawrence J. Merkel	Reg. No. 41,191
Kay A. Colapret	Reg. No. 52,759	Eric B. Meyertons	Reg. No. 34,876
Stephen J. Curran	Reg. No. 50,664	Neal E. Persky	Reg. No. 53,452
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B. Noël Kivlin	Reg. No. 33,929	Mark S. Williams	Reg. No. 50,658
Robert C. Kowert	Reg. No. 39,255		

each of said attorneys or agents being a member or an associate of the firm of Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C., as its attorney or agent for so long as they remain with such firm, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.

Send all future correspondence to:

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Assignee of Interest

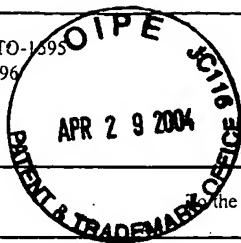
SUN MICROSYSTEMS, INC.
4150 Network Circle
Santa Clara, California 95054

Dated: March 29, 2004

By: Paul D. Sorkin
Paul D. Sorkin, Reg. No. 39,039
Assistant General Counsel

APPENDIX A

MHKKG Ref.	Sun Ref.	Filing Date	Serial #	Title	Inventor(s)
56000	P4637	11/21/00	09/721,029	Single Unit Office Management Center	Talagala, Whay, Wu, Rettberg
37100	P4244PSP	10/30/00	60/244,463	Array of IDE/ATA Disks Emulating SCSI or Fibre Channel Devices	Yatziv
79600	P5480	12/28/00	09/750,555	Method, System, and Program Managing the Rate at Which Client Requests for a Service are Transmitted	Conner
79700	P5624	04/04/01	09/826,081	Method, System, and Program for Enabling Communication Between Devices Using Different Address Formats	Rao
79800	P5781	01/30/01	09/772,626	Method, System, Program, And Data Structures For Testing A Network System Including Input/Output Devices	DeRolf & Kirton
79900	P5786	03/08/01	09/802,229	Method, System, and Program for Determining System Configuration Information	Albright, DeRolf, Gibson, Kirton, & McKenney
80000	P5791	04/04/01	09/826,080	Method, System, and Program for Enabling Communication Between Devices Using Dynamic Addressing	Rao
80100	P5883	03/21/02	10/104,923	Message Routing in a Torus Interconnect	Lee, Talagala, Chong, Wu, & Rettberg
80800	P6196	09/28/01	09/967,141	XOR Processing Incorporating Error Correction Code Data Protection	Acton
80900	P7057	09/28/01	09/967,137	Mapping of Nodes in an Interconnection Fabric	Whay S. Lee
81000	P8430	05/15/03	10/440,218	Method, System, and Program for Allocating Storage Resources	Kumar & Taylor
81100	P8992	03/13/03	10/389,642	Method, System, and Article of Manufacture for Fault Determination	Gibson, McKenney, Cadieux, & Kiser
81200	P6555	06/13/02	10/172,302	System and Method for Isolating Faults in a Network	Wiley, Westminster, & Cadieux
81300	P6248PSP	06/15/01	60/298,658	SAN First Fault Isolation	Wiley, Munson, & Cadieux
81301	P6248	06/13/02	10/172,303	System and Method for Rapid Fault Isolation in a Storage Area Network	Wiley, Munson, & Cadieux
81400	P8966	11/04/03	10/702,150	Using and Generating Finite State Machines to Monitor System Status	Keyur B. Desai
81500	P5445	08/10/01	09/927,985	Method, System, and Program for Managing Multiple Resources in a System	Carlson & da Silva
81600	P5533	04/30/01	09/846,801	Data Integrity Monitoring Storage System	Talagala
81700	P5826	09/28/01	09/967,142	Discovery of Nodes in an Interconnection Fabric	Lee & Mortensen
81800	P7513PSP	02/05/02	60/355,611	Integrated and Automated Storage Management	Amy Lynch
81801	P7513	02/04/03	10/357,554	Method, System, and Program for Supporting a Level of Service for an Application	Amy Elizabeth Lynch & Mark A. Carlson
81900	P5866	06/29/01	09/896,507	Method and Apparatus for Fault Isolation on Network Loops	Gavin Gibson, Sam Gibson, & Yuan Zhang
82000	P7200	06/29/01	09/896,662	Method and Apparatus for Fault Isolation on Network Loops Using Low Level Error Counters	Gavin Gibson, Sam Gibson, & Yuan Zhang



RECORDATION FORM COVER LETTER
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<p>1. Name of conveying party(ies): Keyur B. Desai</p> <p>Execution Date October 10, 2003</p> <p>Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>	<p>2. Name and address of receiving party(ies): Sun Microsystems, Inc. 4150 Network Circle, Santa Clara, California 95054</p>
<p>3. Nature of conveyance:</p> <p><input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger</p> <p><input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name</p> <p><input type="checkbox"/> Other</p>	<p>Additional name(s) & address(es) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>
<p>4. Application number(s) or patent number(s):</p>	
<p>If this document is being filed together with a new application, the execution date of the application is: <u>November 4, 2003</u></p>	
<p>A. Patent Application No.(s):</p>	<p>B. Patent No.(s):</p>
<p>Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>	
<p>5. Name and address of party to whom correspondence concerning document should be mailed:</p> <p>David W. Victor KONRAD RAYNES VICTOR & MANN LLP 315 S. Beverly Drive, Suite 210 Beverly Hills, CA 90212</p>	<p>6. Total number of applications and patents involved: [1]</p>
<p>7. Total fee (37 CFR 3.41).....\$ <u>40</u></p>	
<p><input type="checkbox"/> Enclosed</p> <p><input checked="" type="checkbox"/> Authorized to be charged to deposit account</p>	<p>8. Deposit account number: 50-0585</p>
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David W. Victor
Name of Person Signing

Signature

November 4, 2003

Date

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ASSIGNMENT

Whereas, we

INVENTOR (1) Keyur B. Desai of Broomfield
AND CITY and State of Colorado

have invented certain improvement in

TITLE USING AND GENERATING FINITE STATE MACHINES TO MONITOR SYSTEM STATUS

DATES THAT and executed, respectively, a United States patent application therefore on
INVENTORS (1) 10th October, 2003
SIGNED THE DECLARATION

Whereas, SUN MICROSYSTEMS, INC., a corporation of Delaware, having a place of business at 4150 Network Circle, Santa Clara, California 95054, (hereinafter called SUN MICROSYSTEMS, INC.), desires to acquire the entire right, title and interest in the said application and invention, and to any United States and foreign patents to be obtained therefore;

Now therefore, for a valuable consideration, receipt whereof is hereby acknowledged, I, the above named, hereby sell, assign, and transfer to SUN MICROSYSTEMS, INC., its successors and assigns, the entire right, title and interest in the said application and invention therein disclosed for the United States and foreign countries, and all rights of priority resulting from the filing of said United States application, and I request the Commissioner of Patents to issue any Letters Patent granted upon the invention set forth in said application to SUN MICROSYSTEMS, INC., its successors and assigns; and I hereby agree that SUN MICROSYSTEMS, INC. may apply for foreign Letters Patent on said invention and I will execute all papers necessary in connection with the United States and foreign applications when called upon to do so by SUN MICROSYSTEMS, INC..

Signed and sealed

CITY (1) at B~~roo~~^mfield
DATE on 10th October, 2003,


Keyur B. Desai

SIGNATURE
INVENTOR